

a provision for user input;

a provision for output;

a central processing unit (CPU) coupled to said user input and output;

a monitor for monitoring temperature within said apparatus; and

a clock manager adapted to receive a control signal from said monitor, said clock manager selectively stopping clock signals from being sent to said central processing unit (CPU) when said monitored temperature rises to a level at and above a selected reference temperature level and said CPU is not processing [non-]critical I/O.

6. (amended) An apparatus, comprising:

a provision for user input;

a provision for output;

a central processing unit (CPU) coupled to said user input and output, said central processing unit (CPU) receiving one of a first clock signal at a first speed or a second clock signal at a second speed; and

a clock manager coupled to a monitor that monitors temperature within said apparatus, said clock manager designating that said central processing unit (CPU) receives said first clock signal when said monitored temperature is at a level below a selected reference temperature level and receives said second clock signal when said detected temperature is at a level at and above said selected reference temperature level and said CPU is not processing critical I/O.

9. (amended) An apparatus, comprising:

a provision for user input;

a provision for output;

a central processing unit (CPU) coupled to said user input and output;

a monitor for monitoring temperature within said apparatus; and

a clock manager adapted to receive a control signal from said monitor, said clock manager reducing central processing unit (CPU) clock speed when said detected temperature level is at and above a selected reference temperature level and said CPU is not processing critical I/O.

Cancel ~~Claim~~ 11.

Cancel Claims 13-16.

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17. (amended) A computer, comprising:
means for predicting temperature levels associated with [relevant to] the operation of a central processing unit within said computer; and
means for using said prediction for automatic control of temperature within said computer, said temperature control remaining transparent to a user of said computer.

18. (amended) A computer, comprising:
means for predicting temperature levels associated with the operation of [within] said computer; and
means for using said prediction for automatic temperature control within said computer, said temperature control remaining transparent to a user of said computer.

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21. (amended) An apparatus, comprising:
a central processing unit (CPU);
means for sampling a temperature level within said apparatus; and
means for automatically adjusting the processing speed of said central processing unit (CPU) by modifying the clock signal utilized by the central processing unit (CPU) to maintain said temperature level within said apparatus below a selected reference temperature level when said CPU is not processing critical I/O.

Cancel Claims 24-29.

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30. (amended) The apparatus of Claim 5[1] wherein said clock manager further stops clock signals from being sent to a PCI bus coupled to the central processing unit (CPU).

Cancel Claims 32 and 33.

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36. (amended) The apparatus of Claim 6 [10], wherein said monitor is on board said central processing unit (CPU).